

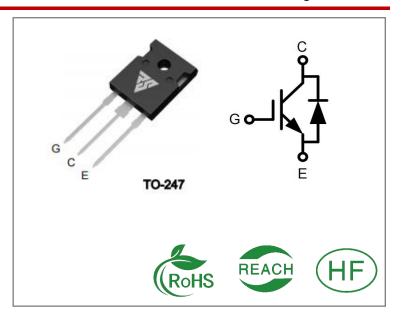
IF	V <sub>ce</sub> (sat )	VCES
75A	1.56V	650V

### **Applications:**

- EV Charging
- Uninterruptible Power Supply (UPS)
- Solar converters

### **Features:**

- 650V trench gate/field termination process
- Very low Vce(sat)
- Low switching loss
- Positive temperature coefficient in Vce(sat)



**Ordering Information** 

Part Number	Package	Marking	Packing	Qty.
RSG75N65UW	T0-247-3	RSG75N65UW	Tube	30 PCS

Absolute Maximun Ratings Tc= 25 °C unless otherwise specified

Symbol	Parameter	RSG75N65UW	Units
VCES	Collector-Emitter Voltage	650	V
VGES	Gate- Emitter Voltage	±20	V
IC	Continuous DC collector current TC = 100 °C	75	А
ICrm	Repetitive peak collector current tp=1 ms	300	А
Ptot	Total Power Dissipation @ TC = 25°C	520	W
Tstg	Operating Junction and Storage Temperature Range	-40to150	°C
TL	Maximum Temperature for Soldering	260	°C

## **Thermal Characteristic**

Symbol	Parameter	RSG75N65UW	Units
R <sub>thJC</sub>	Thermal Resistance, Junction to case for IGBT	0.29	K/W



# **Electrical Characteristics (T**<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
Static Cha	aracteristics						
V(BR)CES	Collector-Emitter Breakdown Voltage	650	-		V	V <sub>GE</sub> =0V,I <sub>CE</sub> =1mA	
ICES	Collector-Emitter Leakage Current	-	-	1	mA	V <sub>GE</sub> =0V, V <sub>CE</sub> =650V	
IGES	Gate to Emitter Leakage current	-	-	200	nA	V <sub>GE</sub> =+20V, V <sub>CE</sub> =0V	
VCE(sat)	Collector-Emitter Saturation Voltage	-	1.56	2	V	I <sub>C</sub> =75A T <sub>j</sub> =25° V <sub>GE</sub> =15 C	
	Gate Threshold Voltage	-	1.9		V	V T <sub>j</sub> =175° C	
VGE(th)	Collector-Emitter Breakdown Voltage	3.8	4.4	5.0	V	I <sub>C</sub> =0.75mA,V <sub>CE</sub> =V <sub>GE</sub>	
Gfs	Transconductance		58		S	I <sub>C</sub> =75A,V <sub>CE</sub> =20V	
Dynamic (	Characteristics						
Cies	Input Capacitance	-	4470			251	
Coes	Output Capacitance	-	170		PF	$V_{CE}$ =25V, $V_{GE}$ =0V,	
Cres	Reverse Transfer Capacitance	-	21			f=100KHz	
Qg	Total Gate Charge		273		nC	IC = 75 A, VGE = 15 V, VCE =520 V	
Switching	Characteristics						
td(ON)	Turn-on Delay Time	-	25				
t <sub>r</sub>	Rise Time	-	130		ns	V <sub>CE</sub> =300V,	
td(OFF)	Turn-Off Delay Time	-	81			I <sub>C</sub> =75A,	
t <sub>f</sub>	Fall Time	-	56			V <sub>GE</sub> =+/-15V,	
E <sub>on</sub>	Turn-On Switching Loss	-	2.65		- mJ	$R_g=8\Omega$ , Inductive Load	
E <sub>off</sub>	Turn-Off Switching Loss	-	1.02		לווו		



# **Diode Maximum Ratings** (TJ= 25 °C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions
VRRM	Repetitive Peak Reverse Voltage	650	V	TC = 25℃
IF	Forward Current	75	Α	TC = 100°C
IFRM	Repetitive Peak Forward Surge Current	300	Α	tp=1 ms

# **Characteristics Values** (T<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	Min.	Тур.	Max.	Test Conditions	Unit
VF	Forward Voltage		1.55 1.69 1.70	2.0	IF =75A,V <sub>GE</sub> =0V TJ = 25°C IF =75A,V <sub>GE</sub> =0V TJ = 150°C IF =75A,V <sub>GE</sub> =0V TJ = 175°C	V
IRM	Peak reverse recovery current		16 26		VR = 300V, IF =75A,V <sub>GE</sub> =-15V diF/dt=500A/us TJ = $25^{\circ}$ C VR = 300V, IF =75A,V <sub>GE</sub> =-15V diF/dt=500A/us TJ = $175^{\circ}$ C	А
Qrr	Reverse Recovery Charge		1.28 3.18		VR = 300V, IF =75A, $V_{GE}$ =-15V diF/dt=500A/us TJ = 25°C VR = 300V, IF =75A, $V_{GE}$ =-15V diF/dt=500A/us TJ = 175°C	uC
trr	Reverse Recovery time		155 225		VR = 300V, IF =75A, $V_{GE}$ =-15V diF/dt=500A/us TJ = 25°C VR = 300V, IF =75A, $V_{GE}$ =-15V diF/dt=500A/us TJ = 175°C	ns
Erec	Reverse recovered energy		0.19		$VR = 300V$ , $IF = 75A$ , $V_{GE} = -15V$ $diF/dt = 500A/us$ $TJ = 25^{\circ}C$ $VR = 300V$ , $IF = 75A$ , $V_{GE} = -15V$ $diF/dt = 500A/us$ $TJ = 175^{\circ}C$	mJ
R <sub>thJC</sub>	Diode Thermal Resistance, Junction		0.35			K/W
Tvj op	Temperature under switching conditions	-40		175		°C



### **Typical Feature Curve**

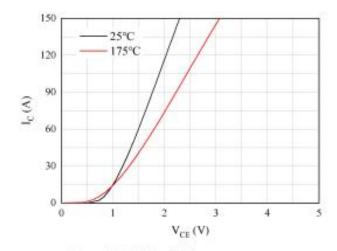


图 1. 典型输出特性 (V<sub>GE</sub>=15V) Figure 1. Typical output characteristics (V<sub>GE</sub>=15V)

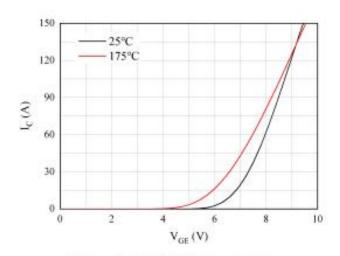


图 3. 典型传输特性(V<sub>CE</sub>=20V) Figure 3. Typical transfer characteristic(V<sub>CE</sub>=20V)

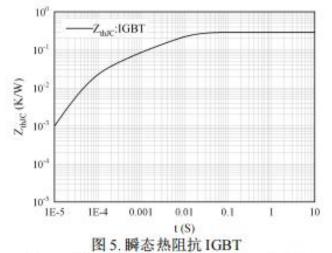


Figure 5. Transient thermal impedance IGBT,  $Z_{thic}=f(t)$ 

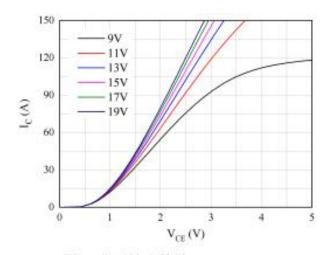


图 2. 典型输出特性 (T<sub>vj</sub>=175℃) Figure 2. Typical output characteristics (T<sub>vj</sub>=175℃)

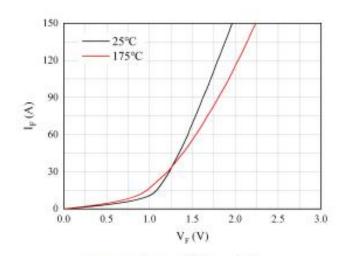


图 4. 正向偏压特性 二极管 Figure 4. Forward characteristic of Diode

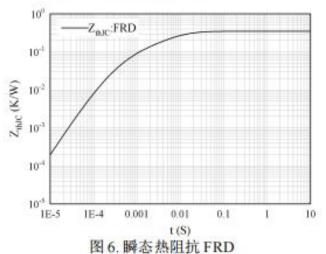


Figure 6. Transient thermal impedance FRD,

Z<sub>thJC</sub>=f(t)

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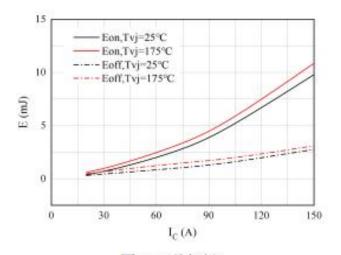


图 7. 开关损耗 Figure 7. Switching losses of IGBT Voz=±15V, Rgon=8Ω, Rgoff=8Ω, Voz=300V

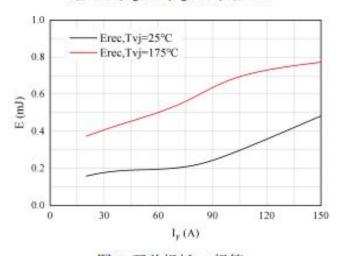


图 9. 开关损耗 二极管 Figure 9. Switching losses of Diode Rgon=80, Vc=300V

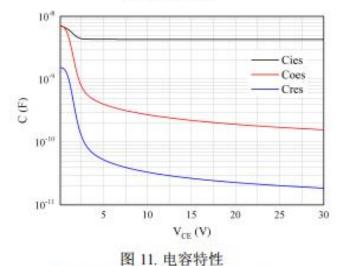


Figure 11. Capacitance characteristic

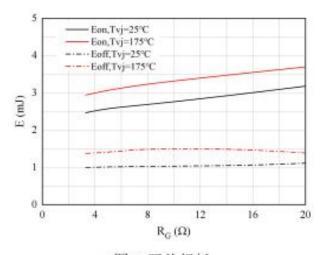


图 8. 开关损耗 Figure 8. Switching losses of IGBT Vox=±15V, Ic=75A, Vcx=300V

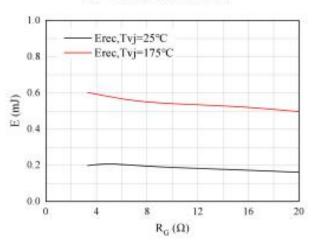
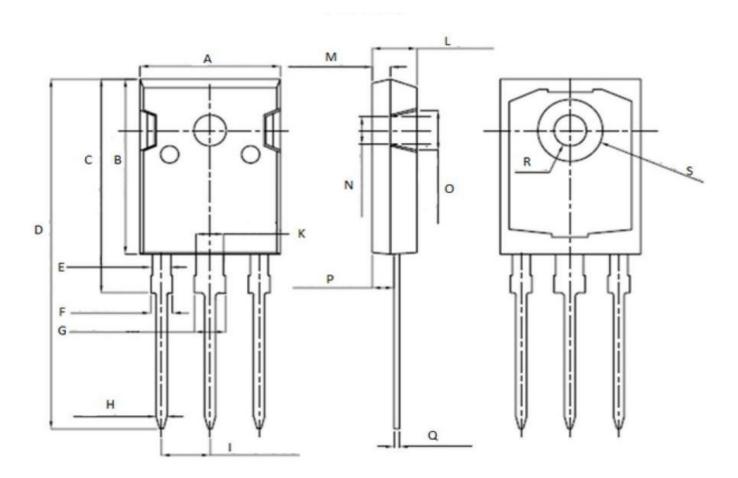


图 10. 开关损耗 二极管 Figure 10. Switching losses of Diode I<sub>2</sub>=75A, V<sub>CZ</sub>=300V



# Package outline drawing(TO-247-3 Unit: mm)



	Unit: mm	
Symbol	Min.	Max.
Α	15. 95	16. 25
В	20.85	21. 25
C	20.95	21. 35
D	40.5	40. 9
E	1.9	2. 1
F	2. 1	2. 25
G	3. 1	3. 25
Н	1.1	1.3
l l	5. 40	5. 50

	Unit: mm	
Symbol	Min.	Max.
K	2.90	3. 10
L	4. 90	5. 30
M	1.90	2. 10
N	4.50	4. 70
0	5.40	5. 60
Р	2. 29	2.49
Q	0. 51	0. 71
R	ф3.5	ф3.7
S	ф7.1	ф7.3



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