

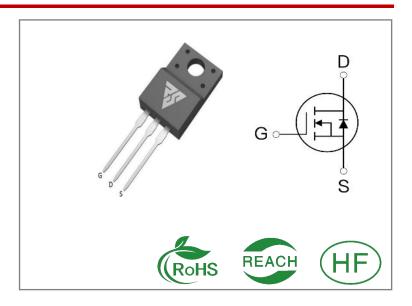
ID	R _{DS} (ON)(Typ)	VDSS
38A	0.09Ω	300V

Applications:

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RSF38N30F	T0-220F	RSF38N30F	Tube	50 PCS

Absolute Maximun Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RSF38N30F	Units
VDSS	Drain-to-Source Voltage	300	V
ID	Continuous Drain Current TC=25℃	38	
IDM	Pulsed Drain Current (Note*1)	152	A
PD	Power Dissipation	36	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10mH, VDD = 50V, RG = 25 Ω	353	mJ
TI TRICO	Maximum Temperature for Soldering	300	
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	260	${\mathbb C}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

^{*} Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the" Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RSF38N30F	Units	Test Conditions
RθJC	Junction-to-Case	3.5	°C/ W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}$ C
RθJA	Junction-to- Ambient	62.5		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25 [°]C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	300			V	VGS=0V,ID=250μA
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=300V,VGS=0 V
	Gate- to- Source Forward Leakage			100		VGS=30V ,VDS=0V
IGSS	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS=0 V

ON Characteristics TJ=25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		0.09	0.11	Ω	VGS=10V,ID=19A
VGS(TH)	Gate Threshold Voltage	3		5	V	VGS=VDS,ID=250μ A

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time	1	63			
trise	Rise Time	-	69			VDS=150V
td(OFF)	Turn- OFF Delay Time		244		nS	ID=38A RG=25Ω
tfall	Fall Time		67			



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		2794	ŀ		VGS=0V
Coss	Output Capacitance		360		pF	VDS=25V
Crss	Reverse Transfer Capacitance		38	-		f=1.0MHz
Qg	Total Gate Charge		72			VDS=240V
Qgs	Gate- to- Source Charge		13		nC	ID=38A
Qgd	Gate-to-Drain(" Miller") Charge		37	-		VGS=10V

Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			38	Α	Integral pn- diode
ISM	Maximum Pulsed Current			152	Α	in MOSFET
VSD	Diode Forward Voltage			1.4	٧	IS=19A,VGS=0V
trr	Reverse Recovery Time		80		nS	VGS=0V
Qrr	Reverse Recovery Charge		0.29		μC	IS=19A,di/dt=100A /μs

Notes:

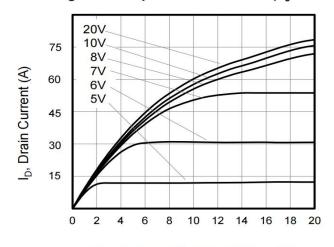
^{* 1.} Repetitive rating, pulse width limited by maximum junction temperature.

^{* 2.} Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%



Typical Feature Curve

Figure 1. Output Characteristics (T_J = 25°C)



 V_{DS} , Drain-to-Source Voltage (V)

Figure 3. Drain Current vs. Temperature

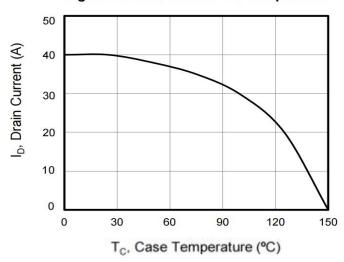


Figure 5. Transfer Characteristics

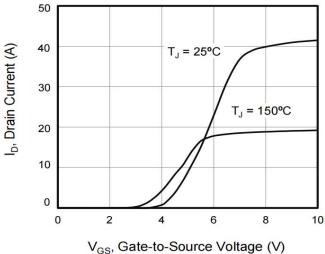


Figure 2. Body Diode Forward Voltage

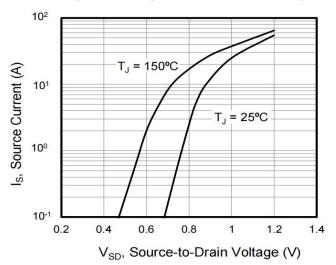


Figure 4. BV_{DSS} Variation vs. Temperature

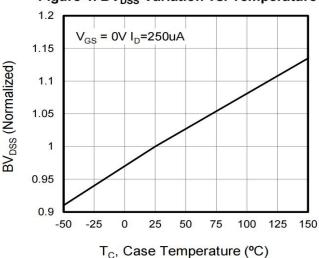
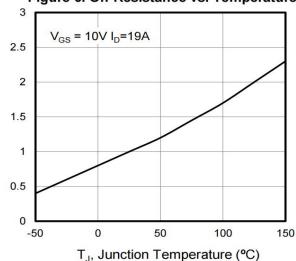


Figure 6. On-Resistance vs. Temperature



R_{DS(on)}, On-Resistance (Normalized)

50

60

70



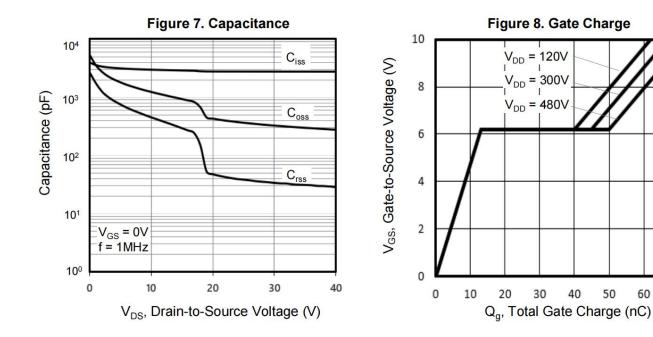
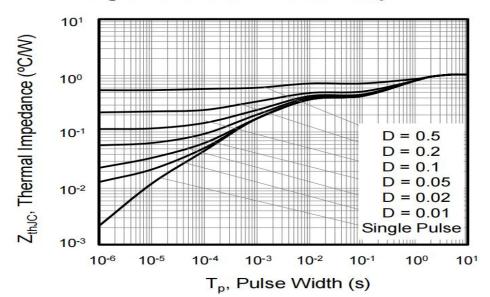
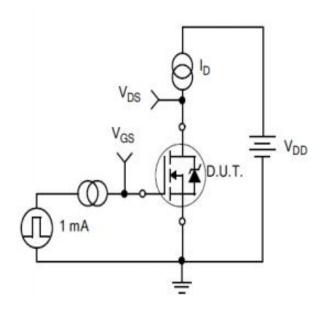


Figure 9. Transient Thermal Impedance



Test Circuits and Waveforms



V_{DS}

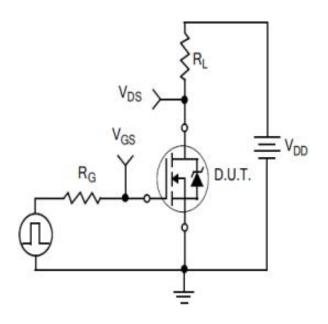
Miller V_{GS}

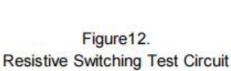
Region Q_{gd}

Q_{gd}
Q_g

Figure 10. Gate Charge Test Circuit

Figure 11. Gate Charge Waveform





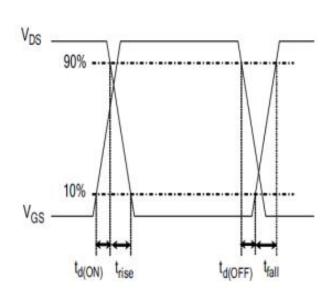


Figure 13.
Resistive Switching Waveforms

Test Circuits and Waveforms

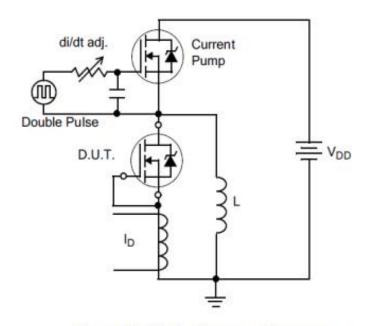


Figure 14. Diode Reverse Recovery
Test Circuit

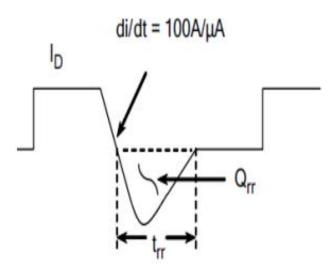


Figure 15. Diode Reverse Recovery Waveform

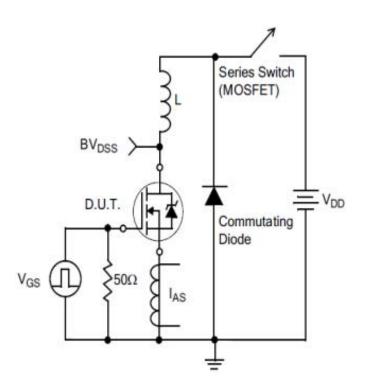


Figure 16. Unclamped Inductive Switching Test Circuit

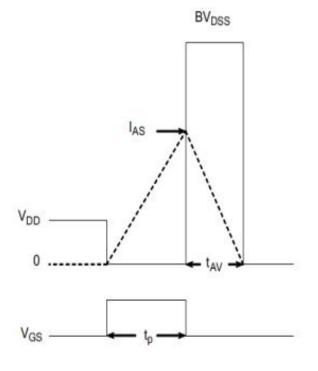
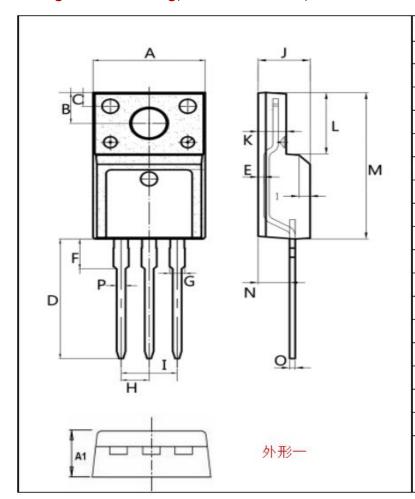


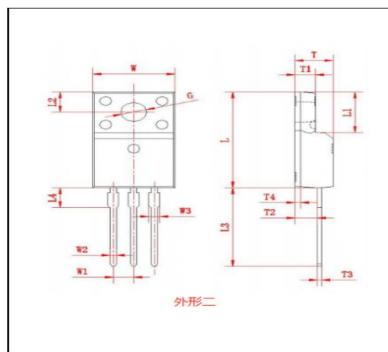
Figure 17. Unclamped Inductive Switching Waveforms



Package outline drawing(TO-220F Unit: mm)



Dim.	Min.	Max.
Α	9.95	10.36
A1	4.5	5.0
В	2.95	3.25
C	1.25	1.45
D	12.60	13.60
E	0.40	0.60
F	2.8	3.5
G	1.30	1.45
Н	(2.54	1)
1	(5.08	3)
J	4.60	4.75
K	2.45	2.65
L	6.5	6.8
М	15.4	16.0
N	2.25	3.05
0	0.45	0.55
Р	0.70	0.90



Dim.	Min.	Max.
W	9.95	10.36
W1	(2.5	4)
W2	0.70	0.90
W3	1.25	1.47
L	15.67	16.07
L1	6.48	6.88
L2	3.2	3.4
L3	12.6	13.6
L4	(3.23	3)
Т	4.50	4.90
T1	2.34	2.74
T2	2.25	2.95
Т3	0.45	0.60
T4	(0.	70)
G	3.08	3.28

All Dimensions in millimeter



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