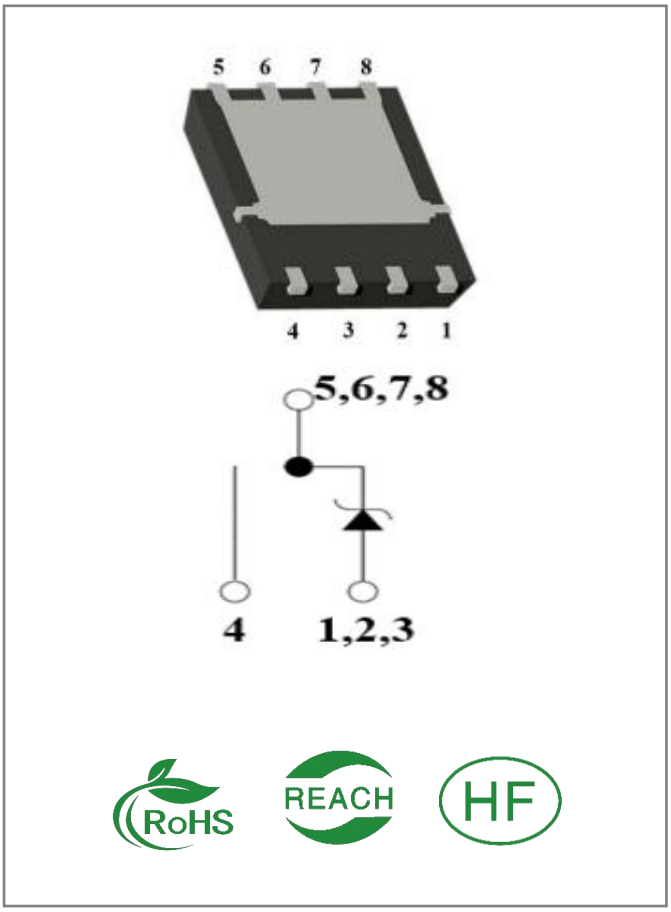
VRRM	IF (TC≤135℃)	QC
650V	7A	13nC



Applications:
- Switch Mode Power Supplies
- Power Factor Correction
- Motor drive, PV Inverter, Wind Power Station

Features:
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- Positive Temperature Coefficient on VF
- Temperature-independent Switching
- 175°C Operating Junction Temperature

Benefits:
- Replace Bipolar with Unipolar Device
- Reduction of Heat Sink Size
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses

Ordering Information

Part Number	Package	Marking	Packing	Qty.
RSS04065G	DFN5*6	RSS04065G	Tape&reel	5000 PCS

Maximum Ratings (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VRRM	Repetitive Peak Reverse Voltage	650	V	TC = 25℃	
VRSM	Surge Peak Reverse Voltage	650	V	TC = 25℃	
VR	DC Blocking Voltage	650	V	TC = 25℃	
IF	Forward Current	14 7 4	A	TC ≤ 25℃ TC ≤ 135℃ TC ≤ 155℃	Fig.3
IFSM	Non-Repetitive Forward Surge Current	42 33	A	TC = 25℃, tp = 10ms, Half Sine Wave TC = 110℃, tp = 10ms, Half Sine Wave	
IFRM	Repetitive Peak Forward Surge Current	37	A	TC = 25℃, tp = 10ms, Half Sine Wave	
Ptot	Power Dissipation	69	W	TC = 25℃	Fig.4
TC	Maximum Case Temperature	155	℃		
TJ,TSTG	Operating Junction and Storage Temperature	-55 to175	℃		

Electrical Characteristics (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
VF	Forward Voltage	1.37 1.78	1.5 -	V	IF = 4A, TJ = 25℃ IF = 4A, TJ = 175℃	Fig.1
IR	Reverse Current	1.1 4.5	50 -	μA	VR = 650V, TJ = 25℃ VR = 650V, TJ = 175℃	Fig.2
C	Total Capacitance	178 25 24	/	pF	VR = 1V, TJ = 25℃, f = 1MHz VR = 200V, TJ = 25℃, f = 1MHz VR = 400V, TJ = 25℃, f = 1MHz	Fig.5
QC	Total Capacitive Charge	13	/	nC	VR =400V,	Fig.6
Ec	Capacitance Stored Energy	2.1		uJ	VR =400V,	Fig.7

Thermal Characteristics (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Typ.	Unit	Note
RθJC	Thermal Resistance from Junction to Case	2.17	℃/W	Fig.8

Typical Feature Curve

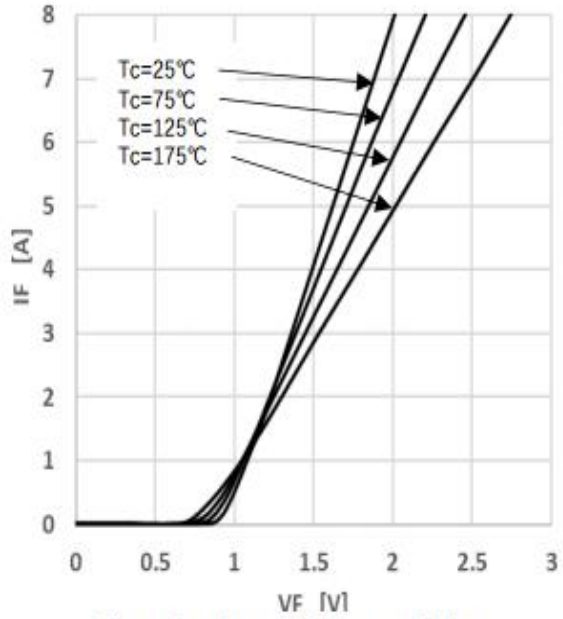


Figure 1 Forward Characteristics

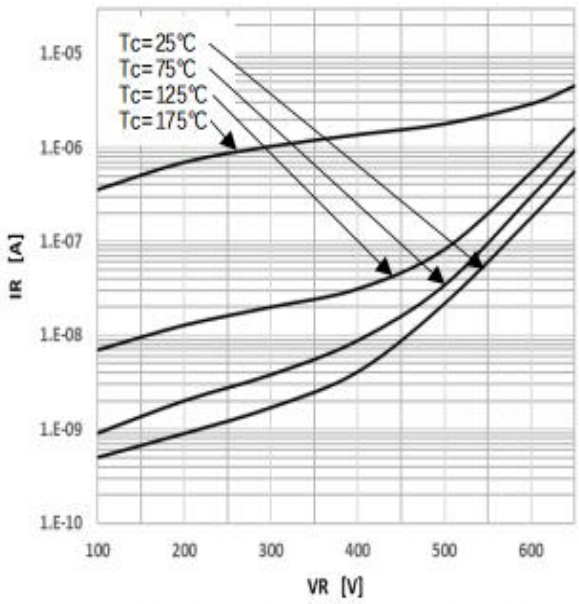


Figure 2 Reverse Characteristics

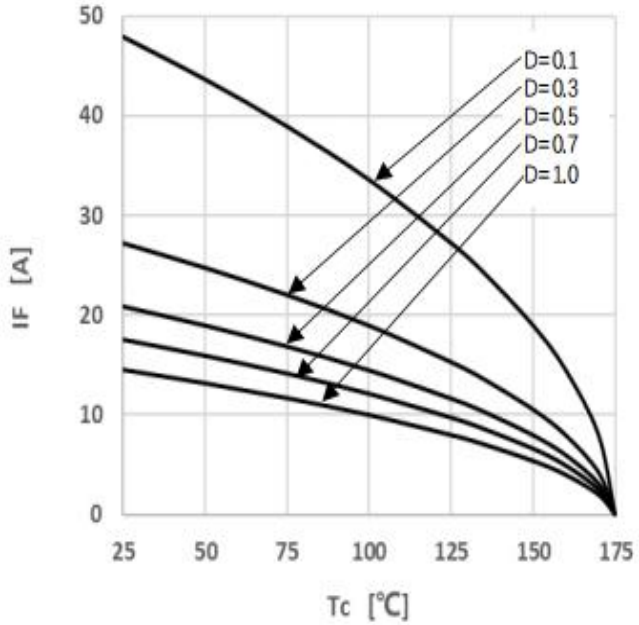


Figure 3 Peak Forward Current Derating

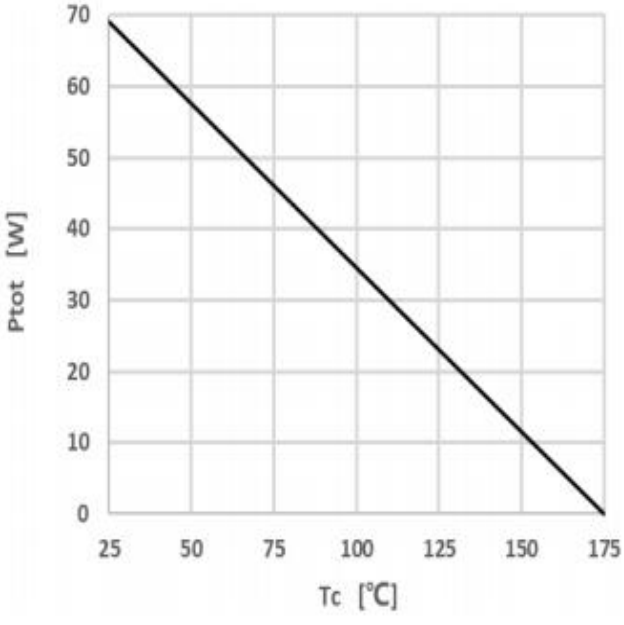


Figure 4 Power Dissipation

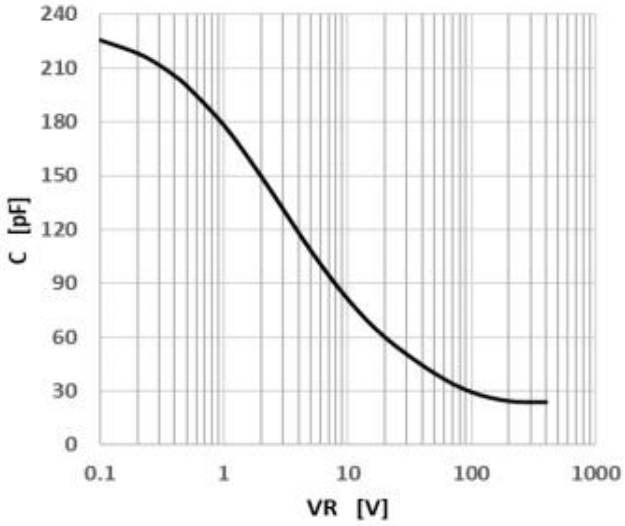


Figure 5 Capacitance vs. Reverse Voltage

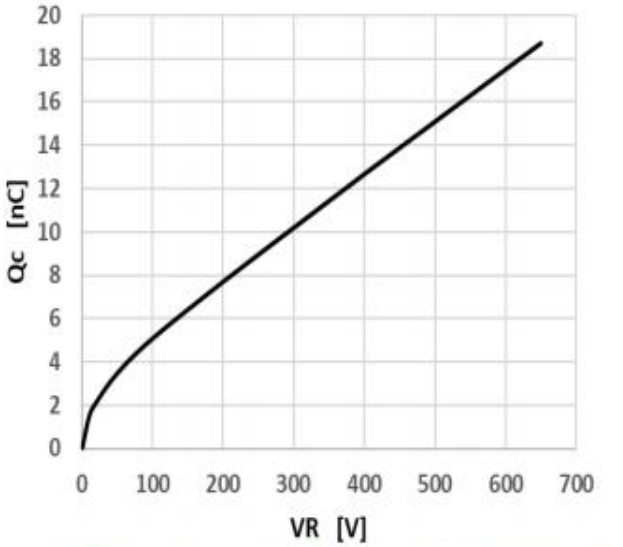


Figure 6 Capacitance Charge vs. Reverse Voltage

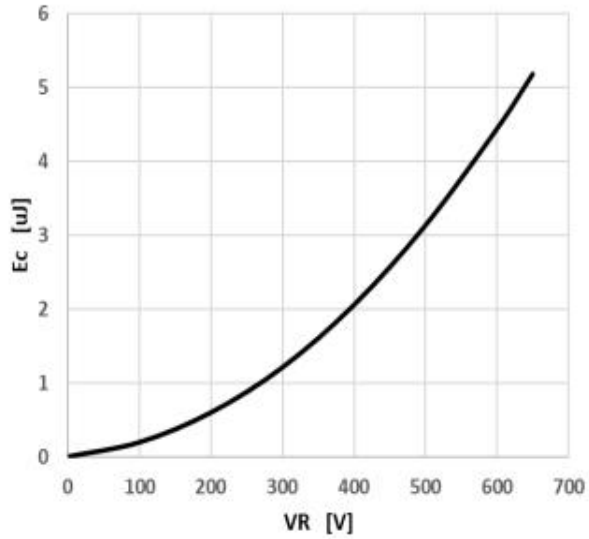


Figure 7 Capacitance Stored Energy

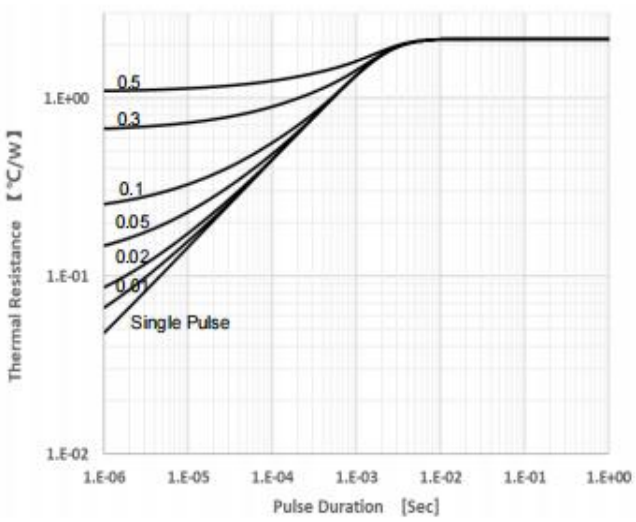
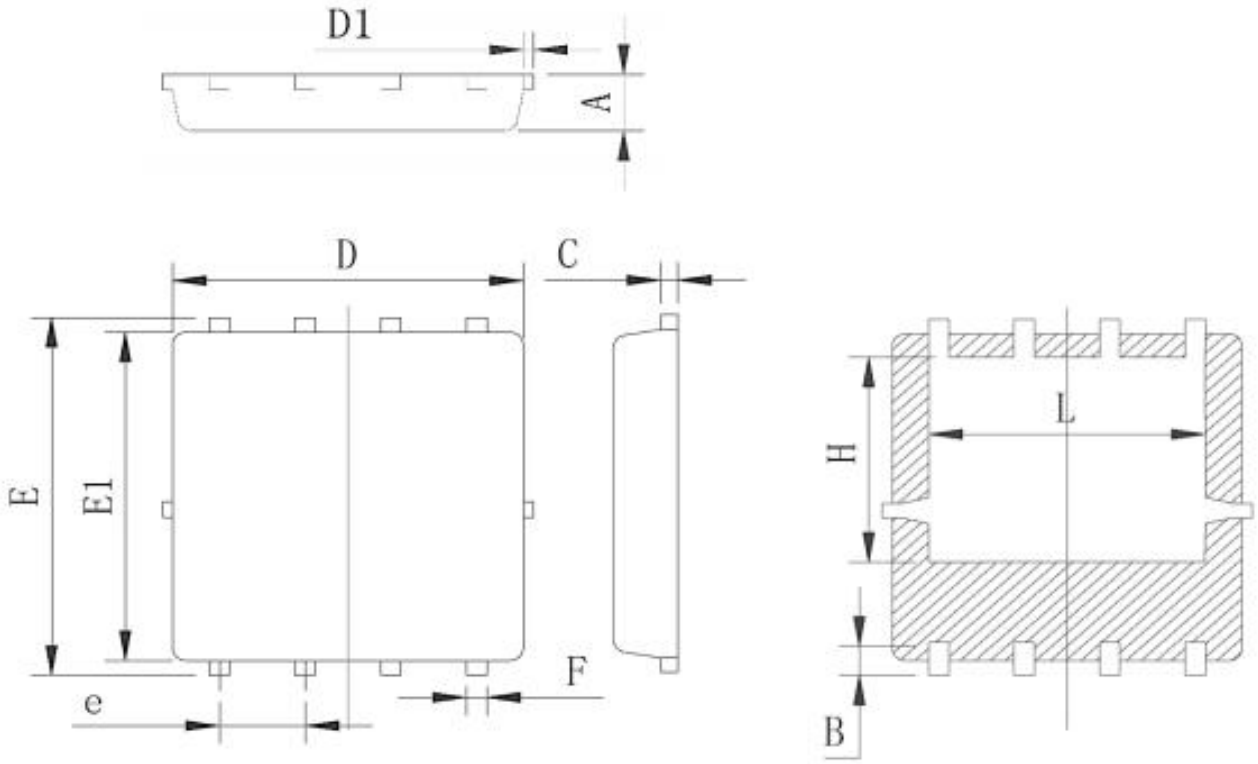


Figure 8 Transient Thermal Impedance

Package outline drawing(DFN5*6 Unit: mm)



Symbol	Min	Typ	Max
A	0.90	0.95	1.00
B	0.48	0.58	0.68
C	0.20	0.254	0.30
D	5.00	5.20	5.40
D1			0.15
E	5.90	6.05	6.20
E1	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
H	3.27	3.47	3.67
L	3.80	4.00	4.20

Disclaimers:

Reasunos Semiconductor Technology Co.Ltd (Reasunos) reserves the right to make changes without notice in order to improve reliability,function or design and to discontinue any product or service without notice .Customers should obtain the latest relevant information before orders and should verify that such information in current and complete.All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology Co.Ltd warrants performance of its hardware products to the speciffications at the time of sale.Testing,reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contr- actual agreement,testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology Co.Ltd does not assume any liability arising from the use of any product or circuit designs described herein.Customers are responsible for their products and applications using Reasunos's components.To minimize risk,customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology Co.Ltd does not warrant or convey any license eith- er expressed or implied under its patent rights,nor the rights of others.Reproduction of inform- ation in Reasunos's data sheeets or data books is permissible only if reproduction is without modification oralteration.Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology Co.Ltd for that product or service voids all exp- ress or implied warrantees for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such statements.

Life Support Policy:

Reasunos Semiconductor Technology Co.Ltd's Products are not authorized for use as cri- tical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology Co.Ltd.
As used herein:
1. Life support devices or systems are devices or systems which: a.are intended for surgical implant into the human body, b.support or sustain life,
c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.

2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system,or to affect its safety or effectiveness.