

VRRM	IF (TC≤135°C)	QC	
650V	25A	62nC	

Applications:

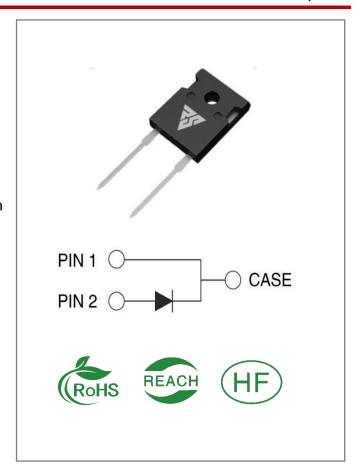
- Switch Mode Power Supplies
- Power Factor Correction
- Motor drive, PV Inverter, Wind Power Station

Features:

- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- Positive Temperature Coefficient on VF
- Temperature-independent Switching
- 175°C Operating Junction Temperature



- Replace Bipolar with Unipolar Device
- Reduction of Heat Sink Size
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses



Ordering Information

Part Number	Part Number Package		Packing	Qty.	
RSS20065W	TO-247-2	RSS20065W	Tube	30 PCS	



Maximum Ratings (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VRRM	Repetitive Peak Reverse Voltage	650	V	TC = 25°C	
VRSM	Surge Peak Reverse Voltage	650	V	TC = 25°C	
VR	DC Blocking Voltage	650	V	TC = 25°C	
IF	Forward Current	54 25 20	А	TC ≤ 25°C TC ≤ 135°C TC ≤ 148°C	Fig.3
IFSM	Non-Repetitive Forward Surge Current	170 154	А	TC = 25° C, tp = 10ms, Half Sine Wave TC = 110° C, tp = 10ms, Half Sine Wave	
IFRM	Repetitive Peak Forward Surge Current	159	Α	TC = 25° C, tp = 10ms, Half Sine Wave	
Ptot	Power Dissipation	204	W	TC = 25°C	Fig.4
TC	Maximum Case Temperature	148	$^{\circ}$		
TJ,TST G	Operating Junction and Storage Temperature	-55 to175	$^{\circ}$		

Electrical Characteristics (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
VF	Forward Voltage	1.35 1.7	1.6 -	V	IF = 20A, TJ = 25℃ IF = 20A, TJ = 175℃	Fig.1
IR	Reverse Current	6 15	100	μΑ	VR = 650V, TJ = 25°C VR = 650V, TJ = 175°C	Fig.2
С	Total Capacitance	906 122 / pF 1MHz VR = 1V, TJ = 25°C, f = 1MHz VR = 200V, TJ = 25°C, f = 1MHz VR = 400V, TJ = 25°C, f = 1MHz		Fig.5		
QC	Total Capacitive Charge	62	/	nC	VR =400V,	Fig.6
Ec	Capacitance Stored Energy	1()		VR =400V,	Fig.7	

Thermal Characteristics (TJ= 25°C unless otherwise specified)

Symbol	Symbol Parameter		Unit	Note
RθJC	Thermal Resistance from Junction to Case	0.735	°C/W	Fig.8



Typical Feature Curve

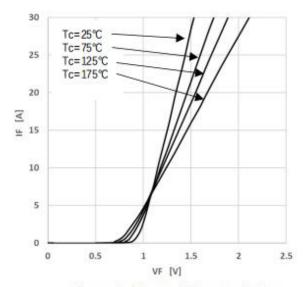


Figure 1 Forward Characteristics

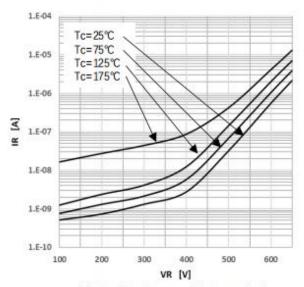


Figure 2 Reverse Characteristics

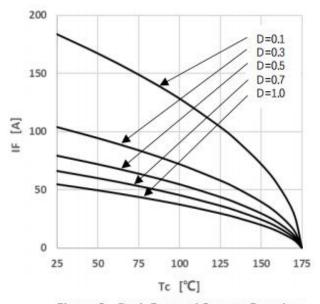


Figure 3 Peak Forward Current Derating

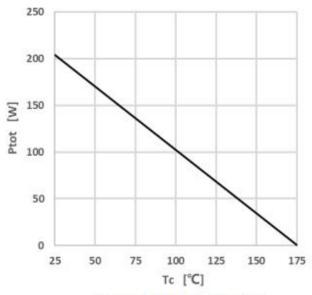


Figure 4 Power Dissipation



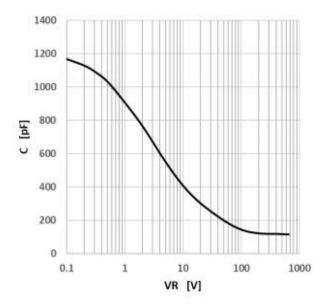


Figure 5 Capacitance vs. Reverse Voltage

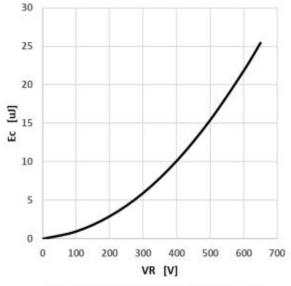


Figure 7 Capacitance Stored Energy

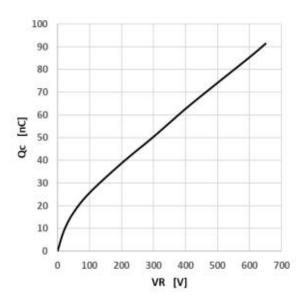


Figure 6 Capacitance Charge vs. Reverse Voltage

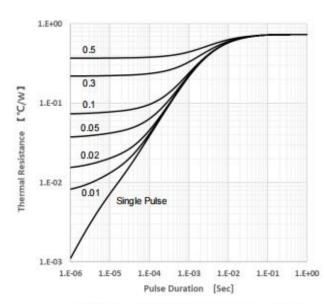
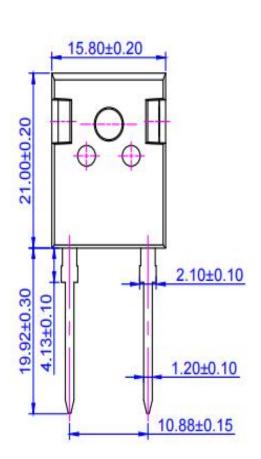
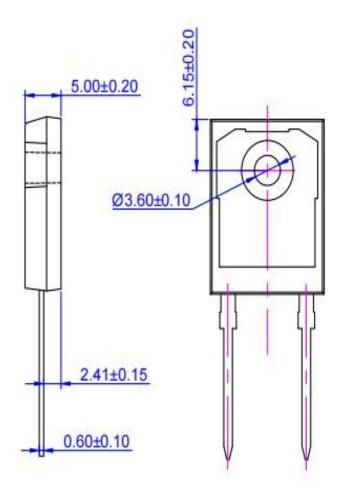


Figure 8 Transient Thermal Impedance



Package outline drawing(TO-247-2 Unit: mm)









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