

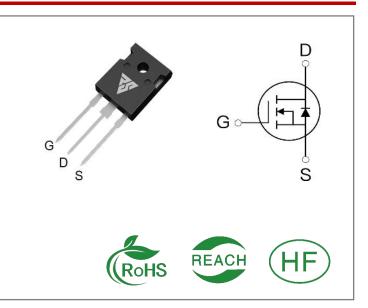
ID	R _{Ds} (ON)(Typ)	VDSS	
30A	85mΩ	500V	

Applications:

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS30N50W	T0-247-3	RS30N50W	Tube	30 PCS

Absolute Maximun Ratings Tc= 25° C unless otherwise specified

Symbol	Parameter	R\$30N50W	Units
VDSS	Drain-to-Source Voltage	500	V
ID	Continuous Drain Current TC=25 $^{\circ}$ C	30	Δ
IDM	Pulsed Drain Current (Note*1)	120	A
PD	Power Dissipation	320	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10mH,,VDD = 50V, RG = 25Ω	2800	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	Ĉ
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the" Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS30N50W	Units	Test Conditions
RØJC	Junction-to-Case	0.38	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^\circ\!\!\mathbb{C}$
RθJA	Junction-to- Ambient	40		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25° C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	500			V	VGS=0V,ID=250μ Α
IDSS	Drain- to- Source Leakage Current			1	μA	VDS=500V,VGS= 0V
	Gate- to- Source Forward Leakage			100	- 4	VGS=30V ,VDS=0 V
IGSS	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS= 0V

ON Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		85	120	mΩ	VGS=10V,ID=15A
VGS(TH)	Gate Threshold Voltage	3		4	V	VGS=VDS,ID=25 0μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		60			
trise	Rise Time		130			VDS=250V
td(OFF)	Turn- OFF Delay Time		100		nS	ID=30A RG=25Ω
tfall	Fall Time		91			



Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		7850			VGS=0V
Coss	Output Capacitance		750		pF	VDS=25V
Crss	Reverse Transfer Capacitance		30			f=1.0MHz
Qg	Total Gate Charge		150			VDS=400V
Qgs	Gate- to- Source Charge		36		nC	ID=30A
Qgd	Gate-to-Drain(" Miller") Charge		56			VGS=10V

Dynamic Characteristics Essentially independent of operating temperature

Source- Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			30	А	Integral pn- diode
ISM	Maximum Pulsed Current			120	А	in MOSFET
VSD	Diode Forward Voltage			1.2	V	IS=15A,VGS=0V
trr	Reverse Recovery Time		500		nS	VGS=0V
Qrr	Reverse Recovery Charge		8.3		μC	IS=30A,di/dt=100 A/µs

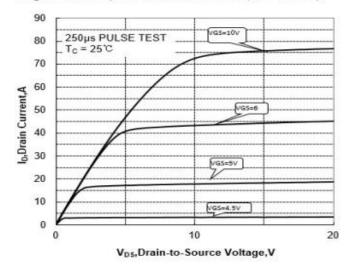
Notes:

- * 1. Repetitive rating, pulse width limited by maximum junction temperature.
- * 2. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%

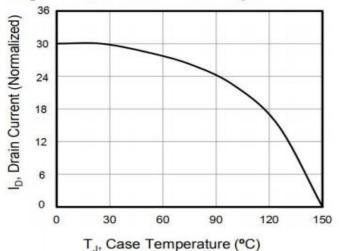


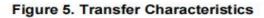
Typical Feature Curve

Figure 1. Output Characteristics (TJ = 25°C)









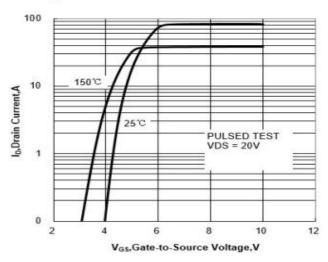
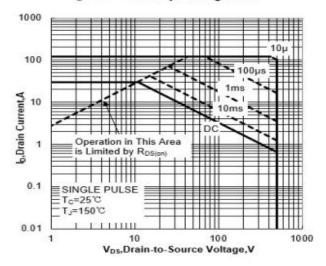


Figure2. Safe Operating Area





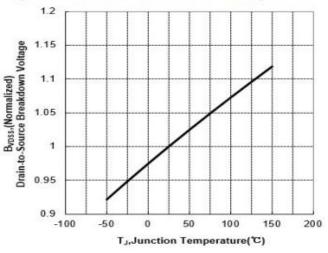
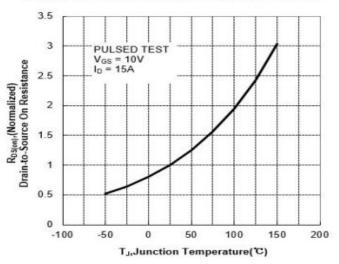
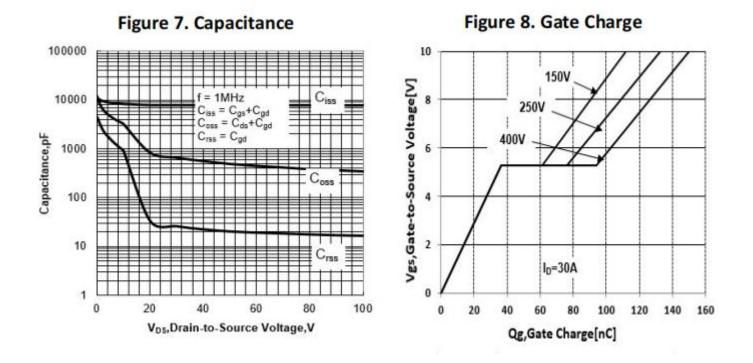


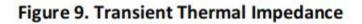
Figure 6. On-Resistance vs. Temperature

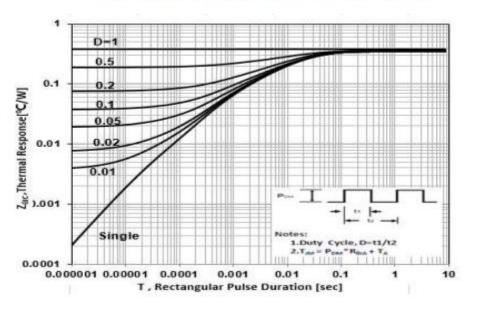


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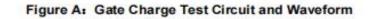


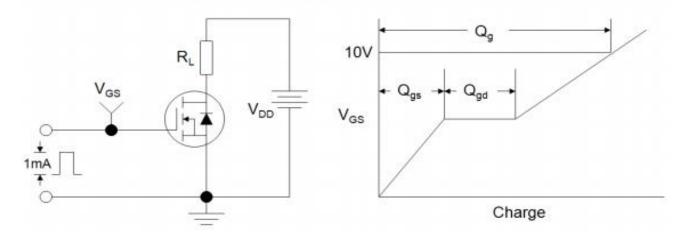






Test Circuits and Waveforms







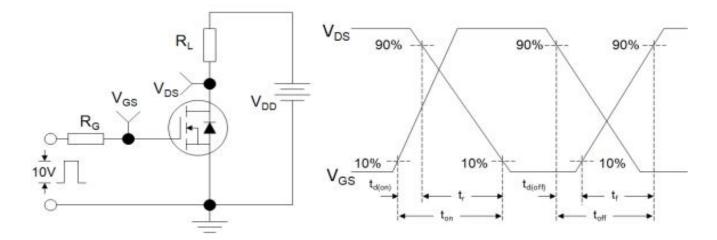
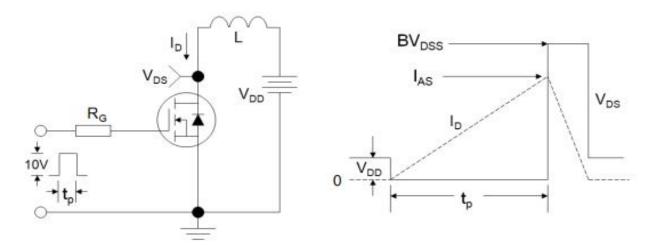
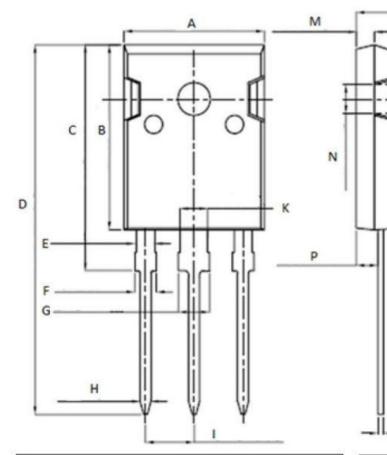


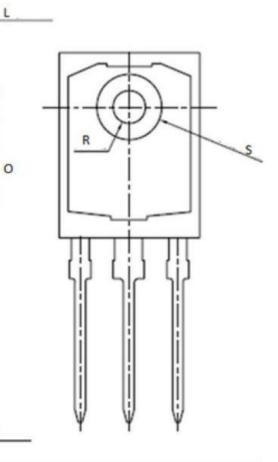
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





Package outline drawing(TO-247 Unit: mm)





	Unit: mm			Unit: mm	
Symbol	Min.	Max.	Symbol	Min.	Max.
Α	15.95	16.25	K	2.90	3.10
В	20.85	21.25	L	4.90	5.30
C	20.95	21.35	M	1.90	2.10
D	40.5	40.9	N	4.50	4.70
E	1.9	2.1	0	5.40	5.60
F	2.1	2.25	Р	2.29	2.49
G	3.1	3. 25	Q	0.51	0.71
Н	1.1	1.3	R	φ3.5	φ3.7
I	5.40	5.50	S	φ7.1	φ7.3

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