

ID	R <sub>Ds</sub> (ON)(Typ)	VDSS
160A	5.2mΩ	150V
• 100% a	: vitching speed avalanche tested ed dv/dt capability	

### **Ordering Information**

Part Number	Package	Marking	Packing	Qty.
RS150N160T	T0-220	RS150N160T	Tube	50 PCS

### Absolute Maximun Ratings Tc= $25^{\circ}$ unless otherwise specified

Symbol	Parameter	RS150N160T	Units
VDSS	Drain-to-Source Voltage	150	V
ID	Continuous Drain Current TC=25℃	160	
ID	Continuous Drain Current TC=100°C	112	А
IDM	Pulsed Drain Current	600	
PD	Power Dissipation	425	W
VGS	Gate- to- Source Voltage	±20	V
EAS	Single Pulse Avalanche Engergy L = 0.3mH,IS =60A, RG = 25Ω, Tj = 25℃	540	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	°C
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

\* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



# **Thermal Resistance**

Symbol	Parameter	RS150N160T	Units	Test Conditions
RØJC	Junction-to-Case	0.42	°C / W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}$ C
RθJA	Junction-to- Ambient	46		1 cubic foot chamber,free air.

### **OFF Characteristics** TJ= $25^{\circ}$ C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	<b>Test Conditions</b>
BVDSS	Drain- to- source Breakdown	150			V	VGS=0V
00000	Voltage	130			V	ID=250μA
IDSS	Drain- to- Source Leakage			1		VDS=120V
1035	Current		T	μA	VGS=0V	
	Gate- to- Source Forward			100		VGS=20V
	Leakage			100		VDS=0V
IGSS	Gate- to- Source Reverse			-100	nA	VGS=-20V
	Leakage			-100		VDS=0V

## **ON Characteristics** TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance		5.2	6.5	mΩ	VGS=10V,ID=20A
VGS(TH )	Gate Threshold Voltage	2.5		4.5	V	VGS=VDS ID=250μA

# **Resistive Switching Characteristics** Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		19		- nS	VDS=75V RL=3.5Ω RG=6Ω VGS=10V
trise	Rise Time		31			
td(OFF)	Turn- OFF Delay Time		52			
tfall	Fall Time		40			



# **Dynamic Characteristics** Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		4300			VGS= 0V
Coss	Output Capacitance		530		pF	VDS=75V
Crss	Reverse Transfer Capacitance		7.5			f=1MHz
Qg	Total Gate Charge		68			VDS= 75V
Qgs	Gate- to- Source Charge		15		nC	ID=20A
Qgd	Gate-to-Drain(" Miller") Charge		14			VGS=10V

### **Source- Drain Diode Characteristics**

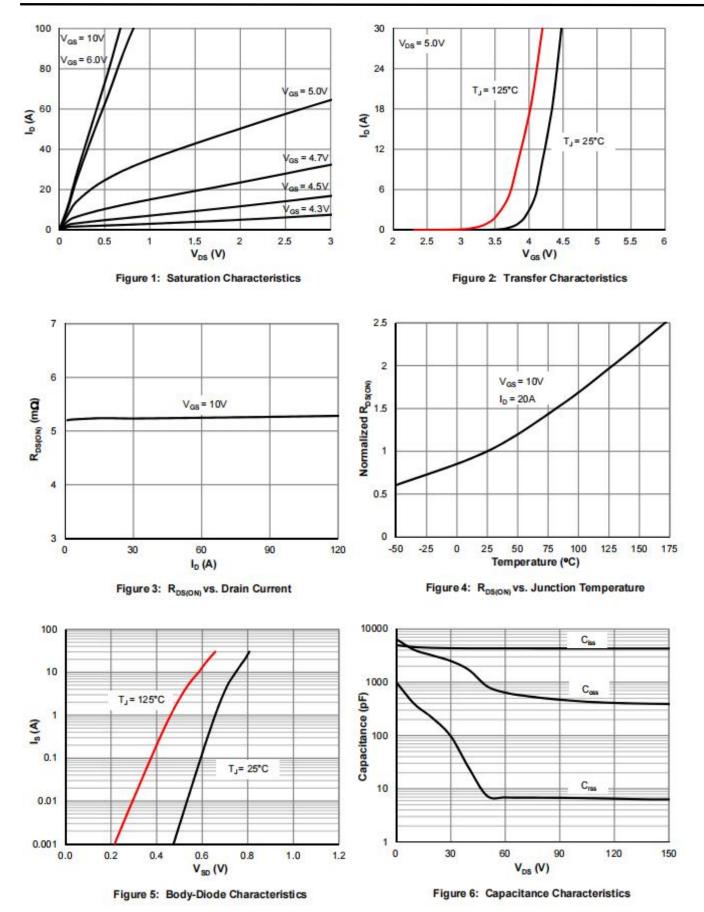
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
IS	Continuous Source Current			160	А	Integral pn- diode	
ISM	Maximum Pulsed Current			600	А	in MOSFET	
VSD	Diode Forward Voltage			1.0	V	IS=1A,VGS=0V	
trr	Reverse Recovery Time		100		nS	VGS=0V	
Qrr	Reverse Recovery Charge		150		nC	IS=15A di/dt=100A/μs	

Notes:

\* 1. Repetitive rating, pulse width limited by maximum junction temperature.

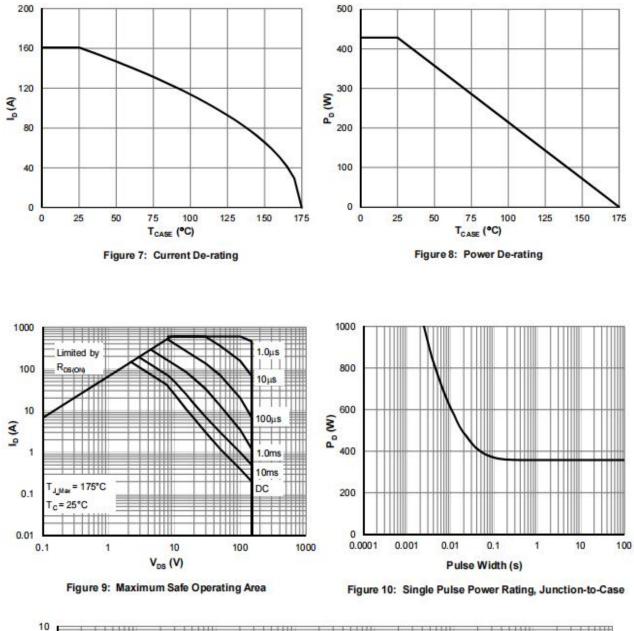
\* 2. Pulse Test: Pulse width  $\leq$  300µs, Duty Cycle  $\leq$  1%

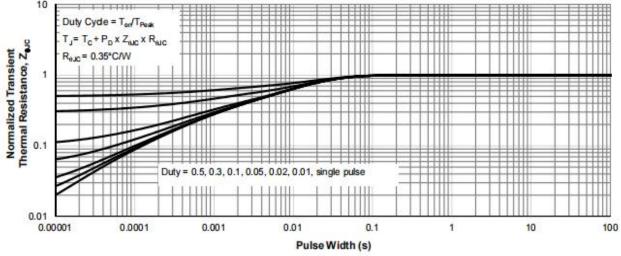




REV:J-B02-05-2024 www.reasunos.com





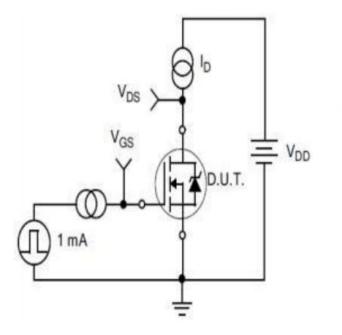




REV:J-B02-05-2024 www.reasunos.com



# Test ircuits and Waveforms



Vgs(TH)

VDS

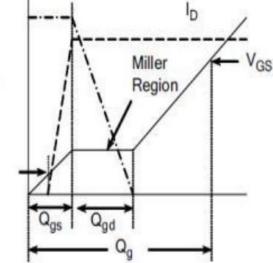


Figure A. Gate Charge Test Circuit

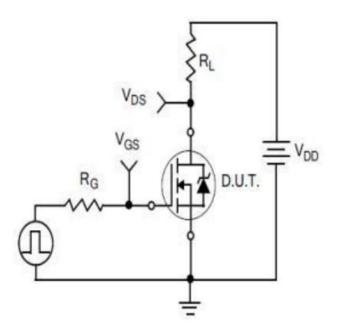


Figure C. Resistive Switching Test Circuit

Figure B. Gate Charge Waveform

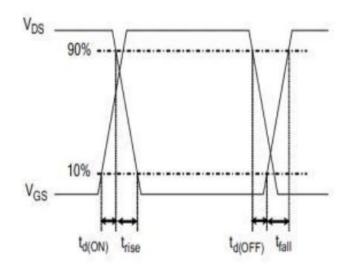
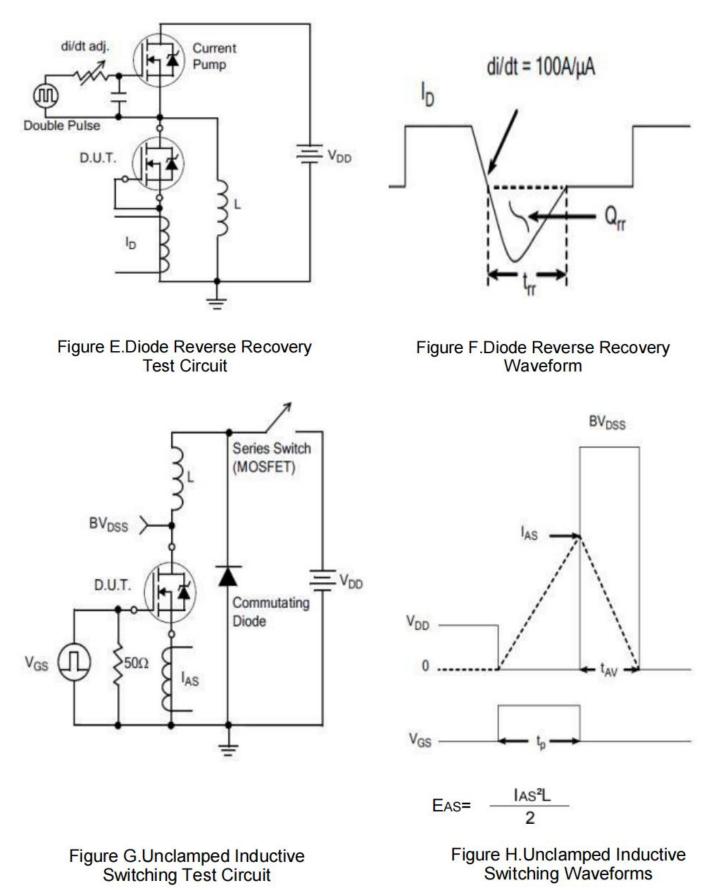


Figure D. Resistive Switching Waveforms

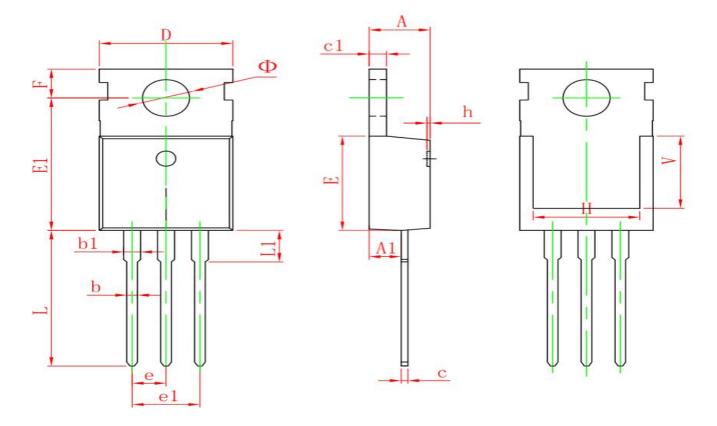


# Test ircuits and Waveforms





# Package outline drawing(TO-220 Unit: mm)



Symbol	Dimensions	In Millimeters	Dimension	s in inches
Symbol	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.950	9.750	0.352	0.384
E1	12.650	13.050	0.498	0.514
е	2.540	TYP.	0.100	) TYP.
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
Н	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	6.900	REF.	0.276	REF.
Ф	3.400	3.800	0.134	0.150



### **Disclaimers:**

Reasunos Semiconductor Technology Co.Ltd (Reasunos) reserves the right to make changes without notice in order to improve reliability,function or design and to discontinue any product or service without notice .Customers should obtain the latest relevant information before orders and should verify that such information in current and complete.All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology Co.Ltd warrants performance of its hardware products to the speciffications at the time of sale.Testing,reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contr- actual agreement,testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology Co.Ltd does not assume any liability arising from the use of any product or circuit designs described herein.Customers are responsible for their products and applications using Reasunos's components.To minimize risk,customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology Co.Ltd does not warrant or convey any license eith- er expressed or implied under its patent rights, nor the rights of others. Reproduction of inform- ation in Reasunos's data sheeets or data books is permissible only if reproduction is without modification oralteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology Co.Ltd for that product or service voids all exp- ress or implied warrantees for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such statements.

### Life Support Policy:

Reasunos Semiconductor Technology Co.Ltd's Products are not authorized for use as cri- tical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology Co.Ltd.

#### As used herein:

1. Life support devices or systems are devices or systems which: a.are intended for surgical implant into the human body, b.support or sustain life,c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.

2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.