

ID	R _{Ds} (ON)(Typ)	VDSS
7.3A	520mΩ	650V

Applications:

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- AC-DC Switching Power Supply

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability

Ordering Information

G	G o S
Rohs	REACH HF

Part Number	Package	Marking	Packing	Qty.
RS65R600F	T0-220F	RS65R600F	Tube	50 PCS

Absolute Maximun Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RS65R600F	Units
VDSS	Drain-to-Source Voltage	650	V
ID	Continuous Drain Current TC=25℃	7.3	
ID	Continuous Drain Current TC=100℃	4.5	A
IDM	Pulsed Drain Current (Note*1)	24	
PD	Power Dissipation	28	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L=10mH,VDS= 50V, RG = 25 Ω , TC=25 $^{\circ}$ C	129	mJ
dv/dt	MOSFET dv/ dt ruggedness VDS = 0400V	50	V/ns
dv/dt	Reverse diode dv/dt VDS = 0400V, Tj = 25° C, ISD≤ID	15	V/ns
	Maximum Temperature for Soldering	300	
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	260	Ĉ
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the" Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS65R600F	Units	Test Conditions
RθJC	Junction-to-Case	4.5	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^\circ C$
RθJA	Junction-to- Ambient	80		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25[°]C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	650			V	VGS=0V,ID=250µA
IDSS	Drain- to- Source Leakage Current			1	μA	VDS=650V,VGS=0 V
	Gate- to- Source Forward Leakage			100	_	VGS=30V,VDS=0V
IGSS	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS=0 V

ON Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		520	600	mΩ	VGS=10V,ID=2A
VGS(TH)	Gate Threshold Voltage	2		4	V	VGS=VDS,ID=250µ A

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		17			
trise	Rise Time		26			VDS=325V
td(OFF)	Turn- OFF Delay Time		53		nS	ID=7.3A RG=25Ω
tfall	Fall Time		38			



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		471			VGS=0V
Coss	Output Capacitance		35		pF	VDS=50V
Crss	Reverse Transfer Capacitance		1.7			f=400kHz
Qg	Total Gate Charge		13			VDS=520V
Qgs	Gate- to- Source Charge		2.1		nC	ID=7.3A
Qgd	Gate-to-Drain(" Miller") Charge		6.9			VGS=10V

Source- Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			7.3	А	Integral pn- diode
ISM	Maximum Pulsed Current			24	А	in MOSFET
VSD	Diode Forward Voltage			1.4	V	IS=7.3A,VGS=0V
trr	Reverse Recovery Time		220		nS	VR=100V
Qrr	Reverse Recovery Charge		2		μC	IS=7.3A,di/dt=100 A/μs

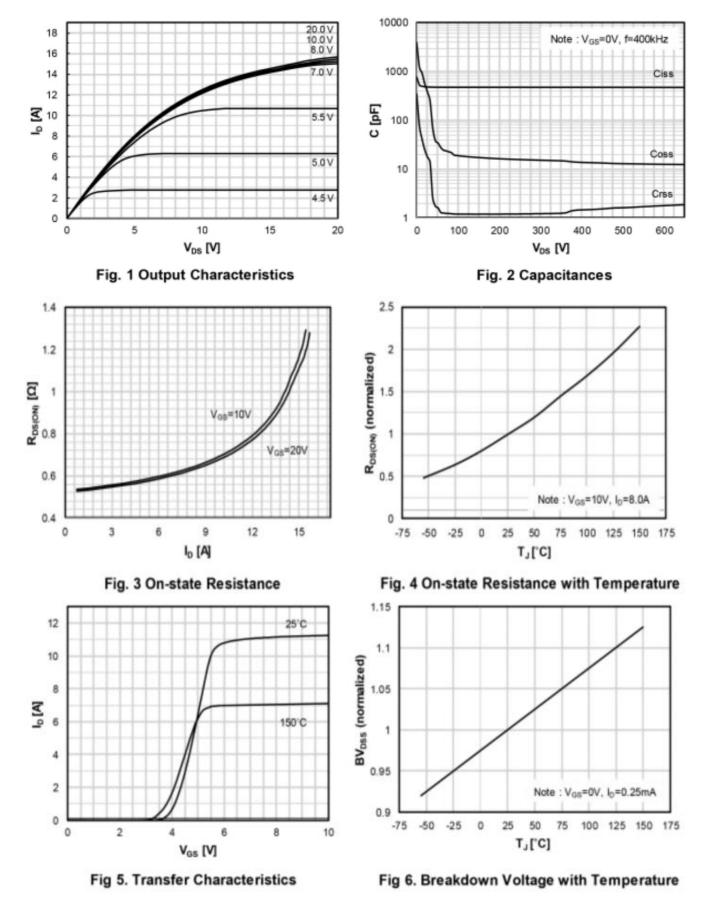
Notes:

* 1. Repetitive rating, pulse width limited by maximum junction temperature.

* 2. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 2%



Typical Feature Curve



REV:P-B01-03-2024 www.reasunos.com



10¹

10⁰

10

10

10

8000 7000

6000 5000

2000 1000

0

105

10

10-3

10⁻²

t_P [s] Fig 11. Power Dissipation

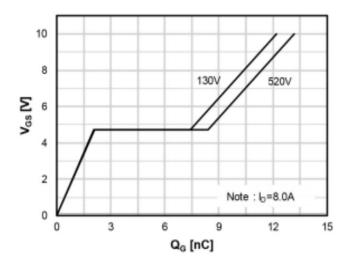
10

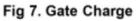
10[°]

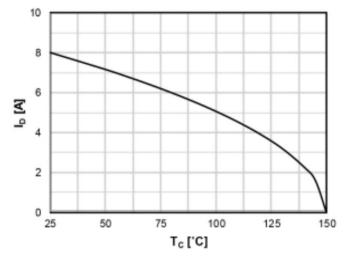
10

PoN 4000 3000

Z_{hjc} ['C/M] 10









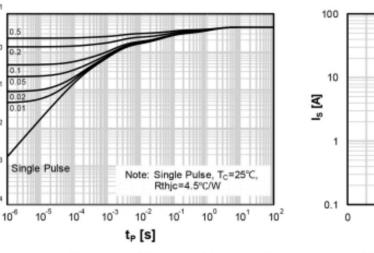


Fig 9. Maximum Transient Thermal Characteristics

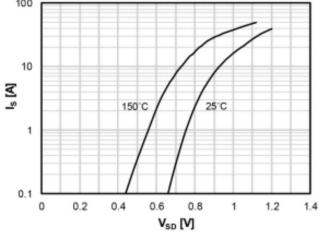


Fig 10. Body Diode Characteristics

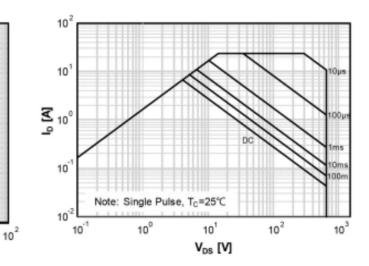
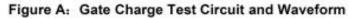


Fig 12. Safe Operating Area

REV:P-B01-03-2024 www.reasunos.com



Test Circuits and Waveforms



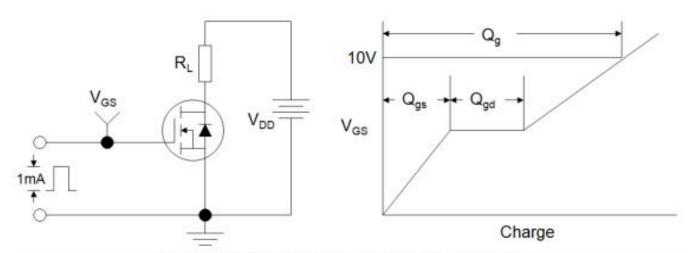


Figure B: Resistive Switching Test Circuit and Waveform

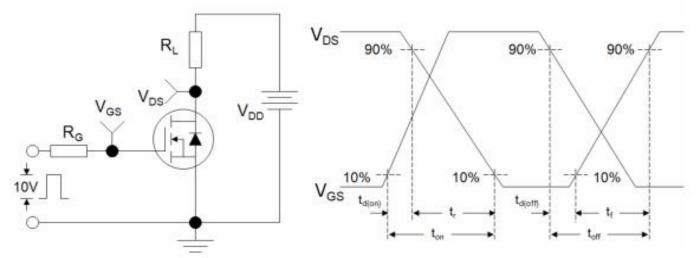
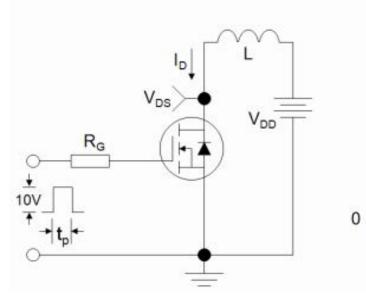
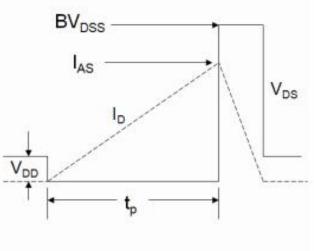


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

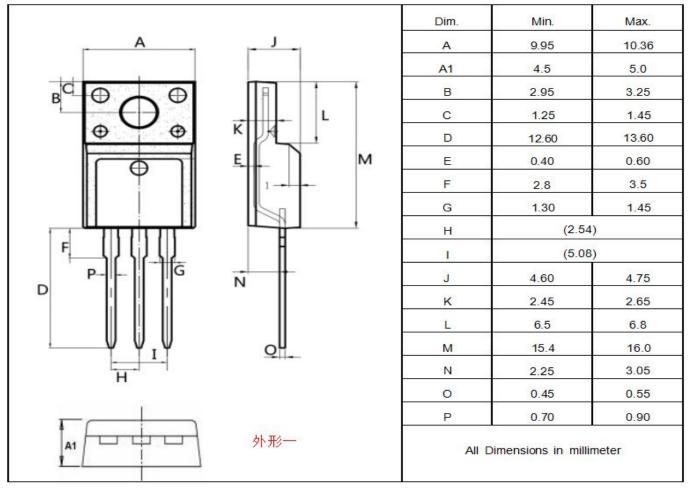


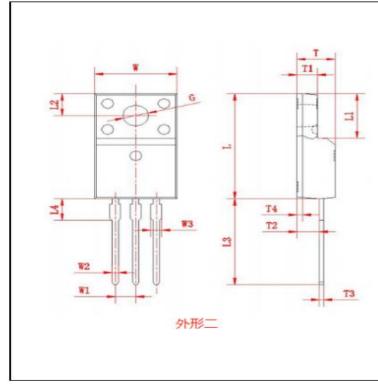


REV:P-B01-03-2024 www.reasunos.com



Package outline drawing(TO-220F Unit: mm)





Dim.	Min.	Max.	
w	9.95	10.36	
W1	(2.54)		
W2	0.70	0.90	
W3	1.25	1.47	
L	15.67	16.07	
L1	6.48	6.88	
L2	3.2	3.4	
L3	12.6	13.6	
L4	(3.23	3)	
т	4.50	4.90	
T1	2.34	2.74	
Т2	2.25	2.95	
T3	0.45	0.60	
T4	(0.	70)	
G	3.08	3.28	



Disclaimers:

Reasunos Semiconductor Technology Co.Ltd (Reasunos) reserves the right to make changes without notice in order to improve reliability,function or design and to discontinue any product or service without notice .Customers should obtain the latest relevant information before orders and should verify that such information in current and complete.All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology Co.Ltd warrants performance of its hardware products to the speciffications at the time of sale.Testing,reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contr- actual agreement,testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology Co.Ltd does not assume any liability arising from the use of any product or circuit designs described herein.Customers are responsible for their products and applications using Reasunos's components.To minimize risk,customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology Co.Ltd does not warrant or convey any license eith- er expressed or implied under its patent rights, nor the rights of others. Reproduction of inform- ation in Reasunos's data sheeets or data books is permissible only if reproduction is without modification oralteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology Co.Ltd for that product or service voids all exp- ress or implied warrantees for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such statements.

Life Support Policy:

Reasunos Semiconductor Technology Co.Ltd's Products are not authorized for use as cri- tical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology Co.Ltd.

As used herein:

1. Life support devices or systems are devices or systems which: a.are intended for surgical implant into the human body, b.support or sustain life,

c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.

2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.