

VDS	RDS(on) Max	ID@25°C
1700V	750mΩ	5A

#### Applications:

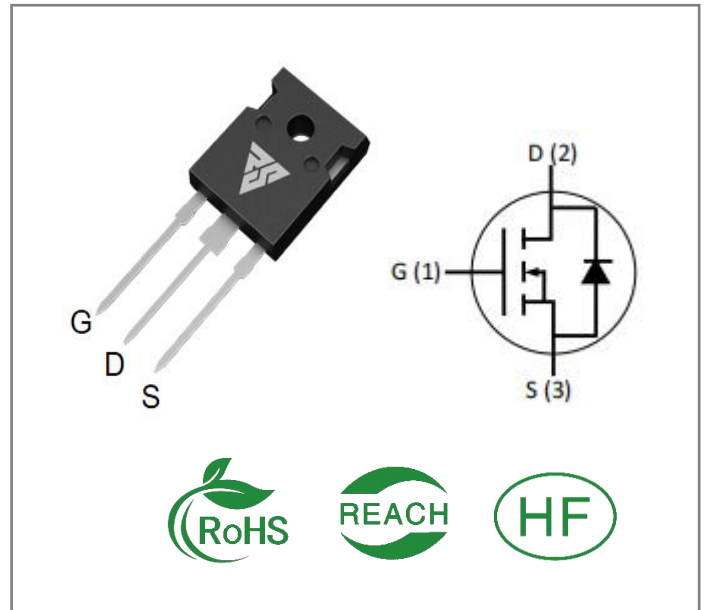
- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- Battery Chargers
- Motor Drives

#### Features:

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness
- Halogen Free, RoHS Compliant

#### Benefits:

- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency



#### Ordering Information

Part Number	Package	Marking	Packing	Qty.
RSM1701K0W	TO-247-3	RSM1701K0W	Tube	30 PCS

#### Maximum Ratings (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VDSmax	Drain - Source Voltage	1700	V	VGS=0V, ID =100μA	
VGSmax	Gate - Source Voltage	-10/+25	V	Absolute maximum values	
VGSop	Gate - Source Voltage	-5/+20	V	Recommended operational values	
ID	Continuous Drain Current	5.0 3.5	A	VGS=20V, TC =25°C VGS=20V, TC =100°C	
PD	Power Dissipation	62	W	TC =25°C, TJ =150°C	
TL	Solder Temperature	260	°C		
TJ, Tstg	Operating Junction and Storage Temperature	-55 to +150	°C		

**Electrical Characteristics** (T<sub>J</sub>= 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V(BR)DSS	Drain-Source Breakdown Voltage	1700			V	V <sub>GS</sub> =0V, I <sub>D</sub> =100μA	
V <sub>GS(th)</sub>	Gate Threshold Voltage	2.0	2.65	4.0	V	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>DS</sub> =5mA TC =25°C	
			1.75		V	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>DS</sub> =5mA TC =150°C	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current		1	100	μA	V <sub>DS</sub> = 1200V, V <sub>GS</sub> =0V	
I <sub>GSS</sub>	Gate-Source Leakage Current		20	200	nA	V <sub>GS</sub> =20V, V <sub>DS</sub> = 0V	
R <sub>DS(on)</sub>	Drain-Source on-state Resistance		650	750	mΩ	V <sub>GS</sub> =20V, I <sub>D</sub> =2A, TC =25°C	
			950			V <sub>GS</sub> =20V, I <sub>D</sub> =2A, TC =150°C	
C <sub>iss</sub>	Input Capacitance		380		pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =1000V f=1MHz, V <sub>AC</sub> =25 mV	
C <sub>oss</sub>	Output Capacitance		14				
C <sub>rss</sub>	Reverse Transfer Capacitance		3.2				
E <sub>ON</sub>	Turn-On Switching Energy		37		μJ	V <sub>DS</sub> =1200V, V <sub>GS</sub> =-5/20V I <sub>D</sub> = 2A, R <sub>G(ext)</sub> = 2.5Ω L= 1478μH	
E <sub>OFF</sub>	Turn-Off Energy		15				
t <sub>d(on)</sub>	Turn-On Delay Time		6.0		ns	V <sub>DS</sub> =1200V V <sub>GS</sub> =-5/20 V I <sub>D</sub> = 2A R <sub>G(ext)</sub> =2. 5 Ω R <sub>L</sub> =600Ω Timing relative to V <sub>DS</sub>	
t <sub>r</sub>	Rise Time		9.5				
t <sub>d(off)</sub>	Turn-Off Delay Time		14.2				
t <sub>f</sub>	Fall Time		23				
R <sub>G(int)</sub>	Internal Gate Resistance		20		Ω	f=1 MHz, V <sub>AC</sub> =25mV	
Q <sub>gs</sub>	Gate to Source Charge		4.8		nC	V <sub>DS</sub> =800V, V <sub>GS</sub> =-5/20V I <sub>D</sub> =2A	
Q <sub>gd</sub>	Gate to Drain Charge		5.6				
Q <sub>g</sub>	Total Gate Charge		13				
G <sub>fs</sub>	Transconductanc		1.2		S	V <sub>GS</sub> =20V, I <sub>D</sub> =2A, TC =25°C	
			1.0			V <sub>GS</sub> =20V, I <sub>D</sub> =2A, TC =150°C	

**Reverse Diode Characteristics** (T<sub>J</sub>= 25°C unless otherwise specified)

Symbol	Parameter	Typ.	Max	Unit	Test Conditions	Note
VSD	Diode Forward Voltage	3.5		V	V <sub>GS</sub> =-5V, I <sub>SD</sub> = 1 A, T <sub>J</sub> = 25°C	
		3.3		V	V <sub>GS</sub> =-5V, I <sub>SD</sub> = 1 A, T <sub>J</sub> = 150°C	
I <sub>S</sub>	Continuous Diode Forward Current		4	A	TC= 25°C	
t <sub>rr</sub>	Reverse Recovery time	22		ns	V <sub>GS</sub> =-5V I <sub>SD</sub> = 2 A V <sub>R</sub> = 1200V dif/dt=1200A/μs	
Q <sub>rr</sub>	Reverse Recovery Charge	31		nC		
I <sub>rrm</sub>	Peak Reverse Recovery Current	3.5		A		

**Thermal Characteristics** (T<sub>J</sub>= 25°C unless otherwise specified)

Symbol	Parameter	Typ.	Unit	Test Conditions	Note
R <sub>θJC</sub>	Thermal Resistance from Junction to Case	1.8	°C/W		
R <sub>θJA</sub>	Thermal Resistance From Junction to Ambient	40			

## Typical Feature Curve

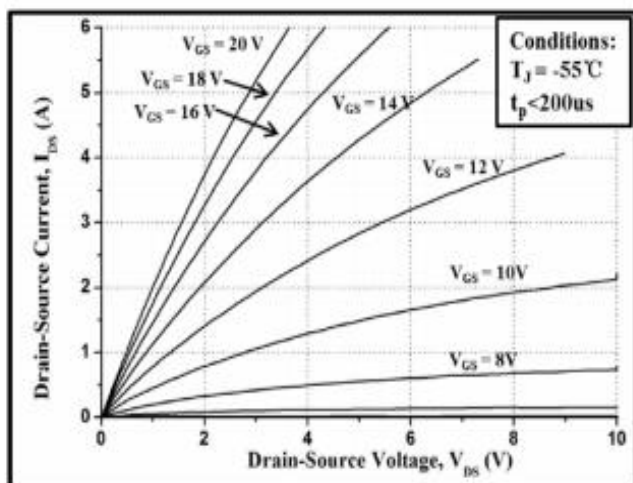


Figure 1. Output Characteristics  $T_J = -55\text{ }^{\circ}\text{C}$

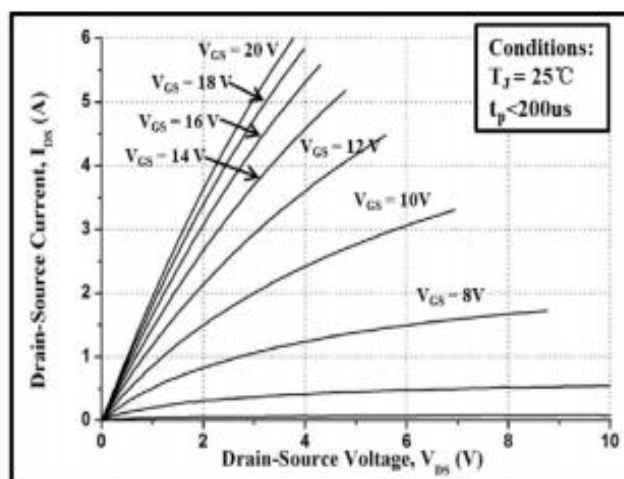


Figure 2. Output Characteristics  $T_J = 25\text{ }^{\circ}\text{C}$

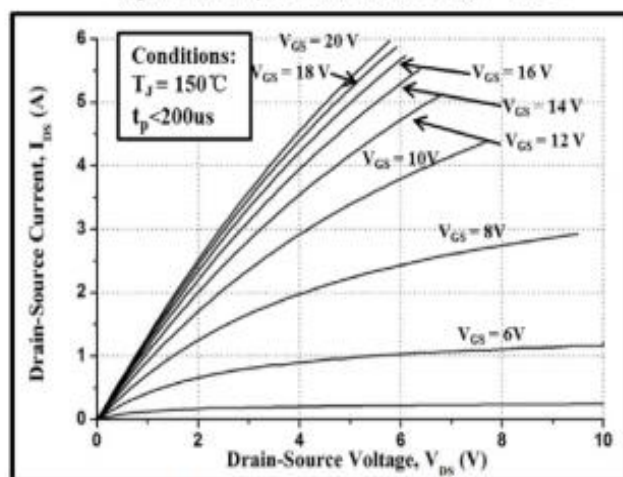


Figure 3. Output Characteristics  $T_J = 150\text{ }^{\circ}\text{C}$

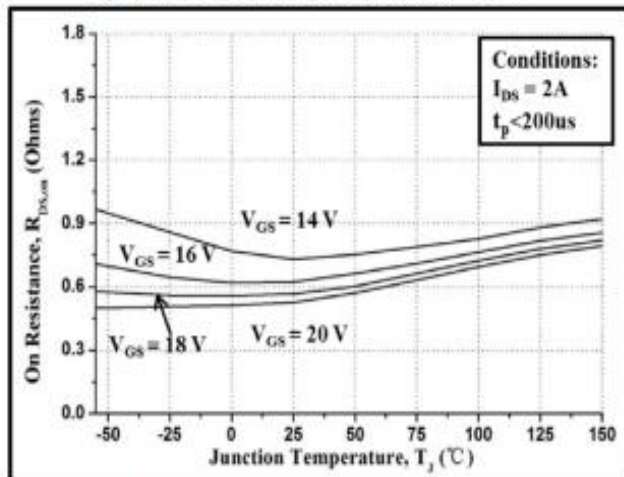


Figure 4. On-Resistance For Various Gate Voltage

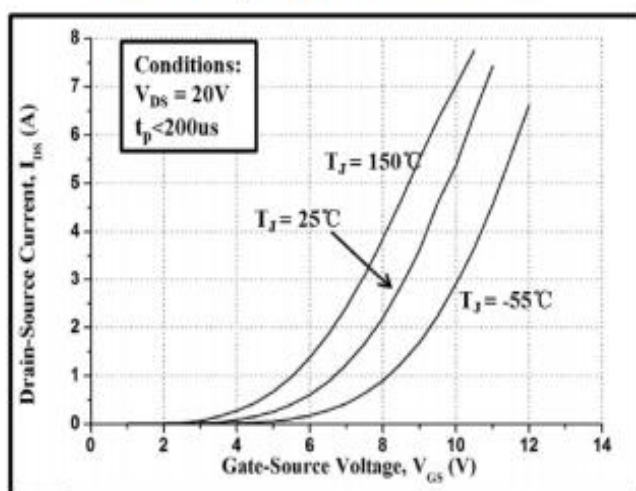


Figure 5. Transfer Characteristic  
for Various Junction Temperatures

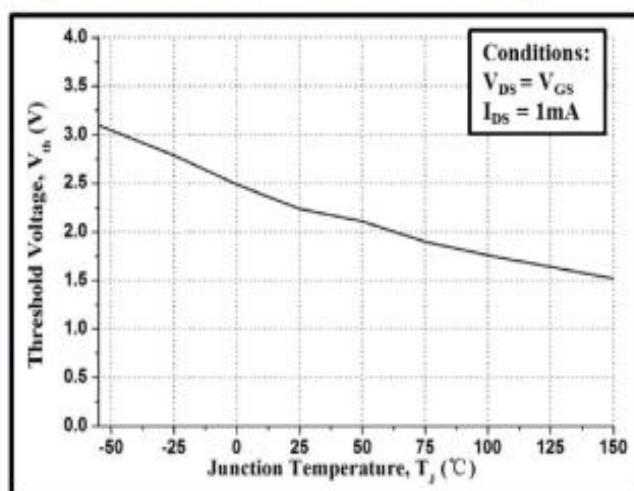


Figure 6. Threshold Voltage vs. Temperature

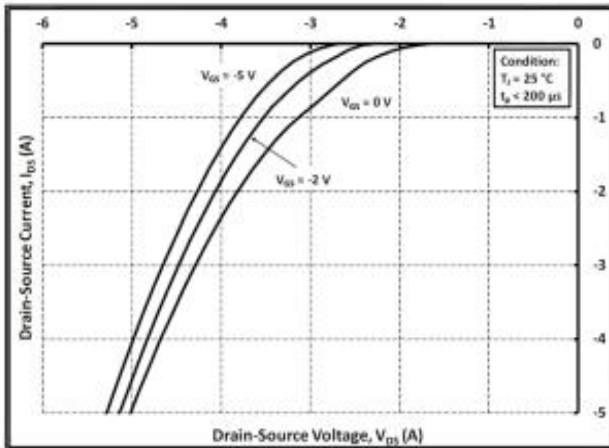


Figure 7. Body Diode Characteristics

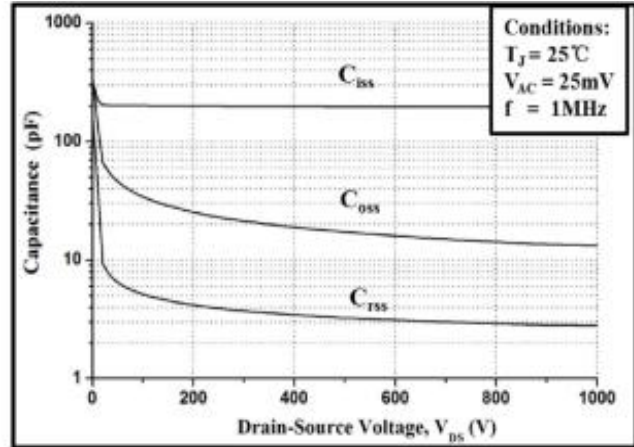


Figure 8. Capacitances vs. Drain-Source Voltage

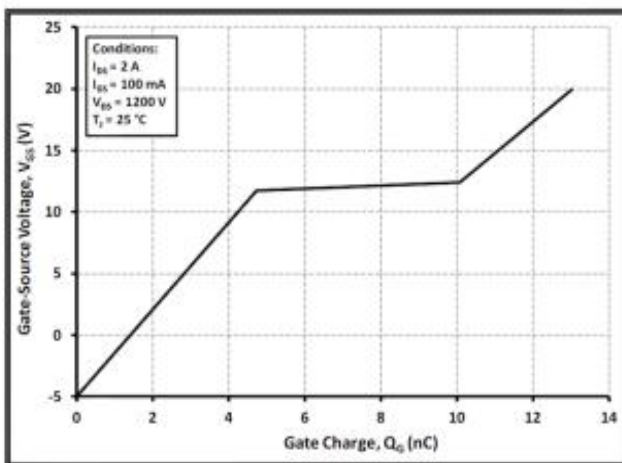


Figure 9. Gate Charge Characteristics

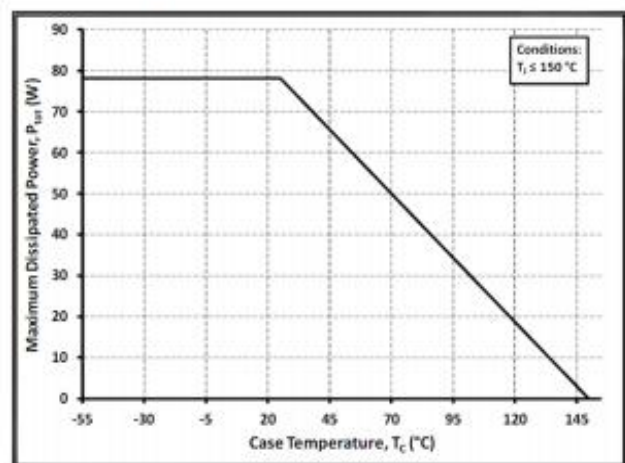


Figure 10. Power Dissipation Derating

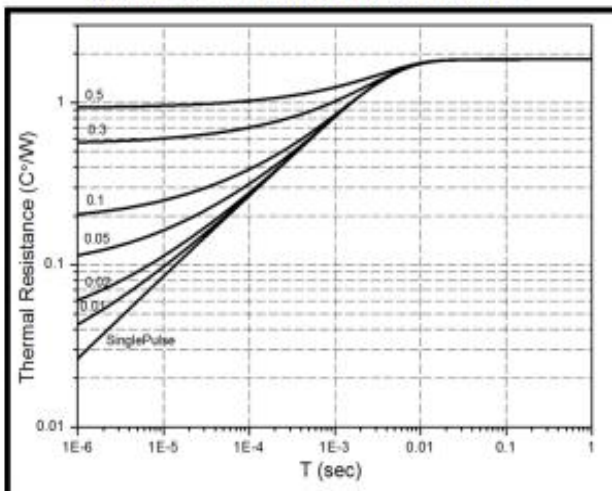
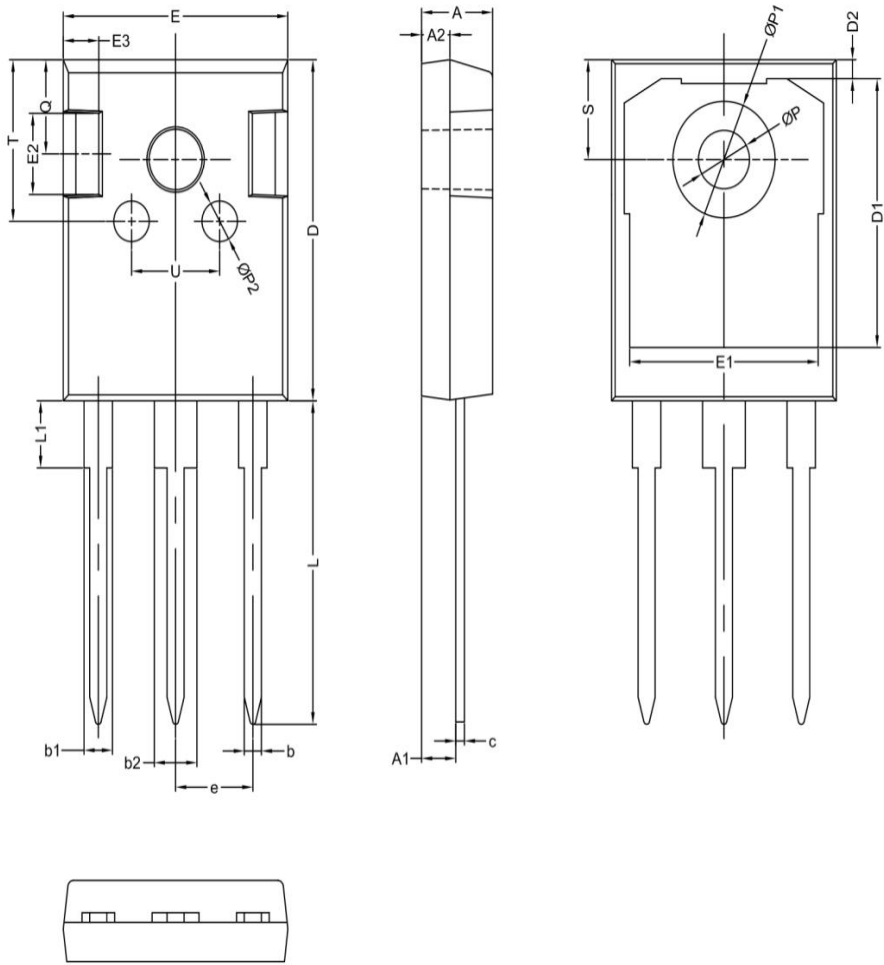


Figure 11. Transient Thermal Impedance

Package outline drawing(TO-247-3 Unit: mm )



符号	机械尺寸/mm		
	最小值	典型值	最大值
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.90	2.00	2.10
b	1.10	1.20	1.35
b1		2.00	
b2		3.00	
c	0.55	0.60	0.75
D	20.80	21.00	21.20
D1		16.55	
D2		1.20	
E	15.60	15.80	16.0
E1		13.30	
E2		5.00	
E3		2.50	
e		5.44	
L	19.42	19.92	20.42
L1		4.13	
P	3.50	3.60	3.70
P1	-	-	7.40
P2		2.50	
Q		5.80	
S	6.05	6.15	6.25
T		10.00	
U		6.20	



**Disclaimers:**

Reasunos Semiconductor Technology Co.Ltd (Reasunos) reserves the right to make changes without notice in order to improve reliability,function or design and to discontinue any product or service without notice .Customers should obtain the latest relevant information before orders and should verify that such information in current and complete.All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology Co.Ltd warrants performance of its hardware products to the specifications at the time of sale.Testing,reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contr- actual agreement,testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology Co.Ltd does not assume any liability arising from the use of any product or circuit designs described herein.Customers are responsible for their products and applications using Reasunos's components.To minimize risk,customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology Co.Ltd does not warrant or convey any license eith- er expressed or implied under its patent rights,nor the rights of others.Reproduction of inform- ation in Reasunos's data sheets or data books is permissible only if reproduction is without modification oralteration.Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology Co.Ltd for that product or service voids all exp- ress or implied warranties for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such statements.

**Life Support Policy:**

Reasunos Semiconductor Technology Co.Ltd's Products are not authorized for use as cri- tical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology Co.Ltd.

As used herein:

1. Life support devices or systems are devices or systems which: a.are intended for surgical implant into the human body, b.support or sustain life,c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.

2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system,or to affect its safety or effectiveness.